### EXERCISE #2

#### OVERVIEW REVIEW

### Write your name and answer the following on a piece of paper

• The companion to static analysis (analysis without running the target program) is dynamic analysis (analysis that includes running the target program). Give an example of a dynamic analysis.

- Miss a class? It's not too late to get points for the check-in assignment!
- The Entry Survey: results and thoughts

"Please record lectures"

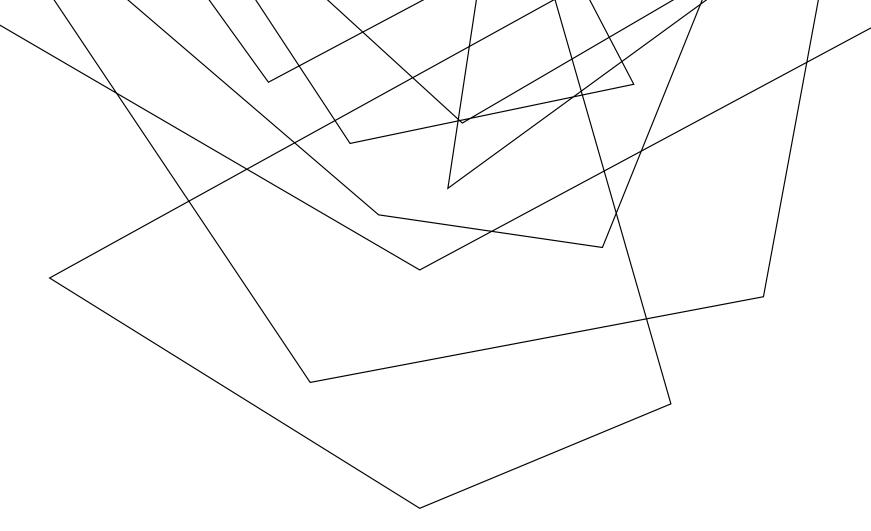
"My given name is <X> but I go by <Y>"

Lots of interest in learning about vulnerabilities

Some concern about workload

How long are the quizzes?

"I'll probably be late a lot"



## VISUALIZING CODE

EECS 677: Software Security Evaluation

Drew Davidson

# LAST TIME: OVERVIEW

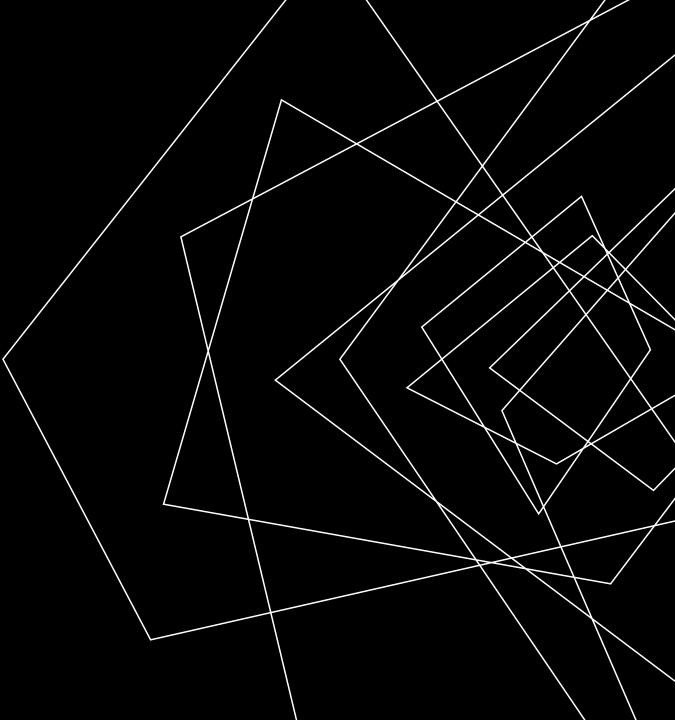
Discussed the insufficiency of manual source code analysis for security evaluation.

# Described the need to deal with abstractions of software. These abstractions can do two things:

- Emphasize some under-appreciated aspect of the target program
- Simplify or ease a form of reasoning about the target program

## **LECTURE OUTLINE**

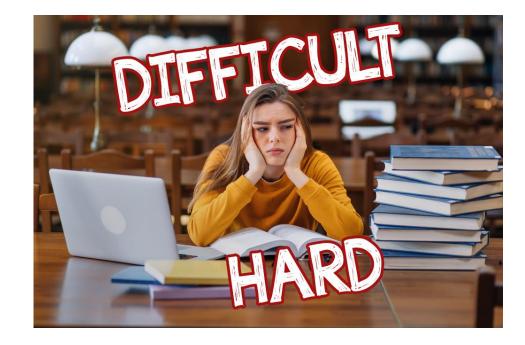
- Instruction Flowcharts
- Control Flow Graphs



# VISUALIZING PROGRAMS

#### **Reading code is hard!**

 It's really important to determine how code *flows* from one instruction to the next



#### **CODE GRAPHS** INSTRUCTION FLOWCHARTS

#### Program analysis relies heavily on two questions

- (How) can we get to a particular program point?
- What is the program configuration at a given point?

### Helpful to structure program instructions as a graph

- Visualize transfer of control
- Avail ourselves of graph analyses (e.g. reachabilty)

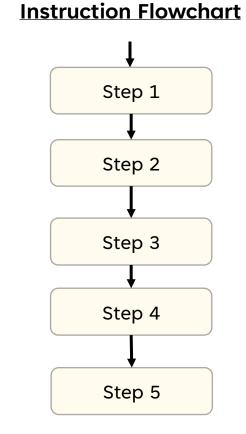


#### **FLOWCHARTS** ABSTRACTING CODE: INSTRUCTION FLOWCHARTS

### **NOTATION** NODES ARE INSTRUCTIONS EDGES GO TO SUCCESSOR NODE

#### **OPERATION**

EXECUTE CURRENT INSTRUCTION PROCEED TO SUCCESSOR NODE



## FLOWCHART EXAMPLE – HOW TO FLOSS

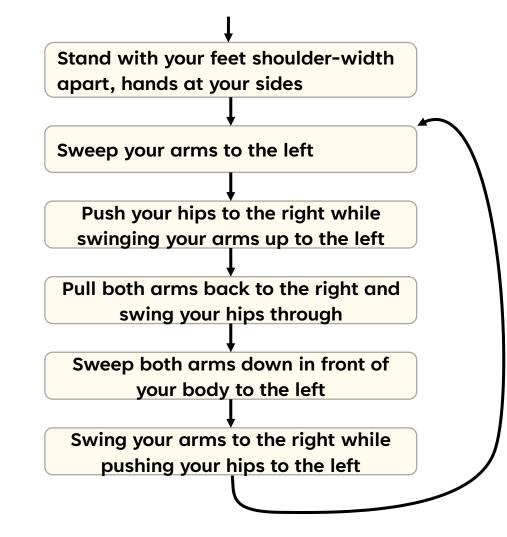
**ABSTRACTING CODE: INSTRUCTION FLOWCHARTS** 

### **NOTATION** NODES ARE INSTRUCTIONS EDGES GO TO SUCCESSOR NODE

#### OPERATION

EXECUTE CURRENT INSTRUCTION PROCEED TO SUCCESSOR NODE

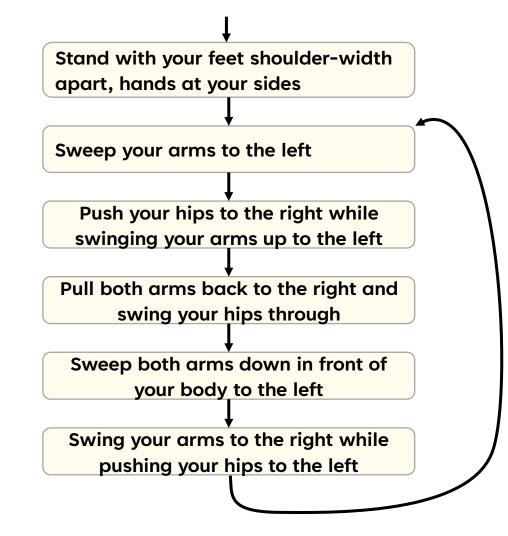




## FLOWCHARTS – CONDITIONALS

**ABSTRACTING CODE: INSTRUCTION FLOWCHARTS** 

### NOTATION NODES ARE INSTRUCTIONS EDGES GO TO SUCCESSOR NODES (DISAMBIGUATED WITH CONDITIONS) OPERATION EXECUTE CURRENT INSTRUCTION PROCEED TO SUCCESSOR NODE (ACCORDING TO CONDITION)



## FLOWCHARTS – CONDITIONALS

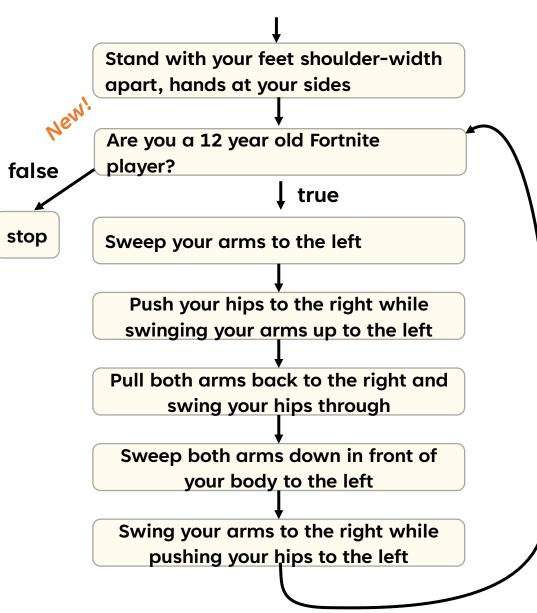
**ABSTRACTING CODE: INSTRUCTION FLOWCHARTS** 

#### NOTATION

NODES ARE INSTRUCTIONS EDGES GO TO SUCCESSOR NODES (DISAMBIGUATED WITH CONDITIONS)

#### **OPERATION**

EXECUTE CURRENT INSTRUCTION PROCEED TO SUCCESSOR NODE (ACCORDING TO CONDITION)



## FLOWCHARTS – FOR CODE?!?!!!

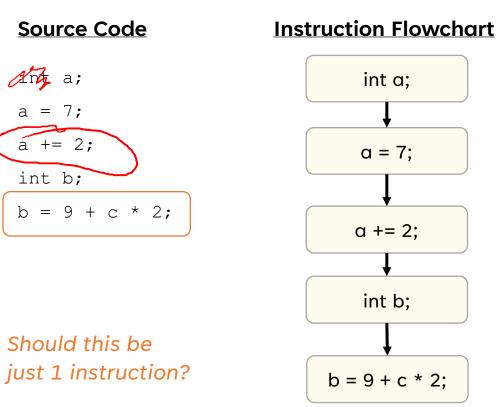
ABSTRACTING CODE: INSTRUCTION FLOWCHARTS

#### NOTATION

NODES ARE INSTRUCTIONS EDGES GO TO SUCCESSOR NODES (DISAMBIGUATED WITH CONDITIONS)

#### **OPERATION**

EXECUTE CURRENT INSTRUCTION PROCEED TO SUCCESSOR NODE (ACCORDING TO CONDITION)



# CODE FLOWCHARTS

#### NOTATION

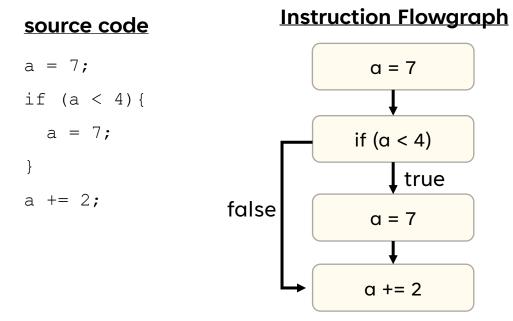
NODES ARE INSTRUCTIONS

EDGES GO TO SUCCESSOR NODES UNDER APPROPRIATE CONDITION

#### **OPERATION**

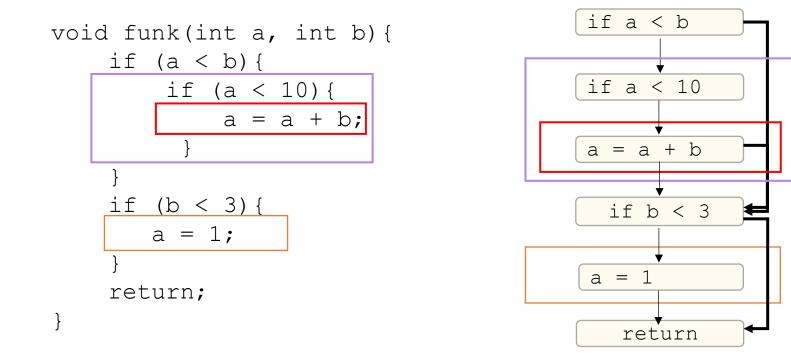
EXECUTE CURRENT

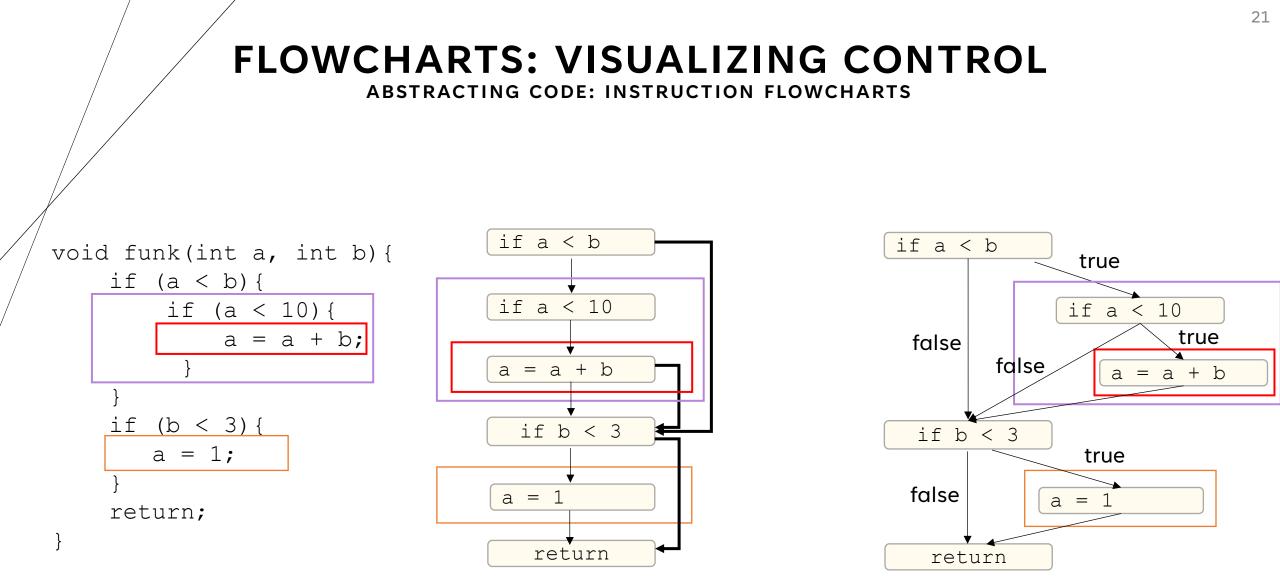
PROCEED TO THE RIGHT SUCCESSOR



### FLOWCHARTS: VISUALIZING CONTROL

**ABSTRACTING CODE: INSTRUCTION FLOWCHARTS** 





## FLOWCHARTS: A USEFUL TOOL

ABSTRACTING CODE: INSTRUCTION FLOWCHARTS

#### MAYBE THIS IS HOW YOU LEARNED TO THINK ABOUT CODE!

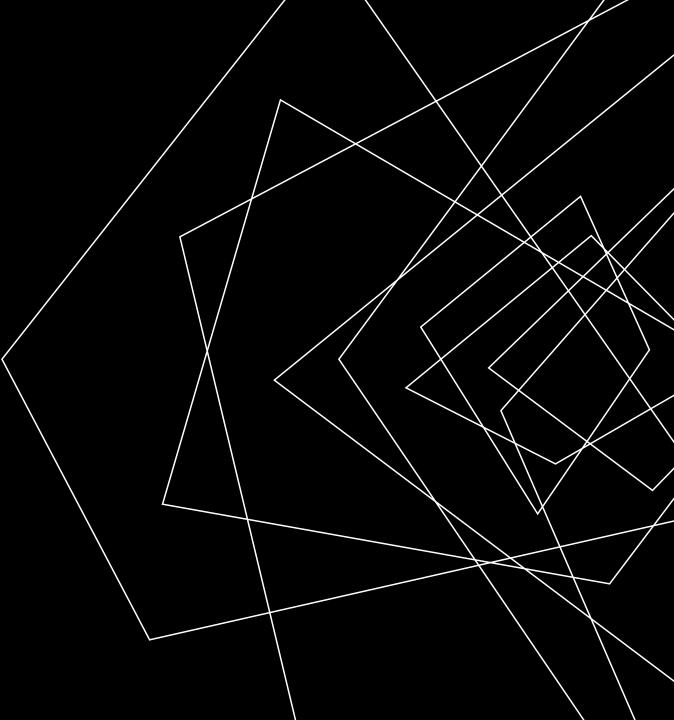
IT'S A NICE WAY TO VISUALIZE THE CONTROL FLOW OF THE PROGRAM

WE CAN EXTEND THIS INTUITION FOR PROGRAM ANALYSIS



## **LECTURE OUTLINE**

- Instruction Flowcharts
- Control Flow Graphs



## **COMPACTING THE FLOW CHART**

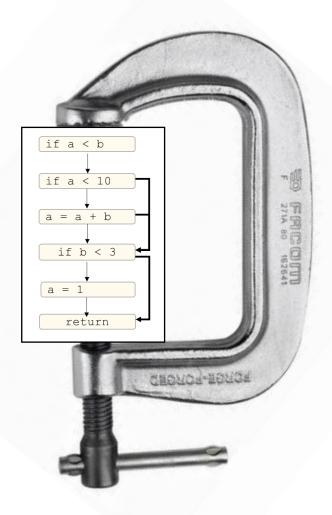
ABSTRACTING CODE: CONTROL-FLOW GRAPHS

## FROM FLOWCHARTS TO CONTROL FLOW GRAPHS

- This graph is needlessly verbose
- Too many nodes that communicate nothing

#### WHAT IF WE ELIMINATE THE 1 INSTRUCTION PER NODE CONSTRAINT?

 Attempt to use as few edges as possible



#### **BASIC BLOCKS** ABSTRACTING CODE: CONTROL-FLOW GRAPHS

#### DEFINITION: SEQUENCE OF INSTRUCTIONS GUARANTEED TO EXECUTE WITHOUT INTERRUPTION





## **BASIC BLOCKS BOUNDARIES**

ABSTRACTING CODE: CONTROL-FLOW GRAPHS

## TWO DISTINGUISHED INSTRUCTIONS IN A BLOCK (MAY BE THE SAME INSTRUCTION)

- Leader: An instruction that begins the block
- Terminator: An instruction that ends the block







## **BASIC BLOCKS BOUNDARIES**

ABSTRACTING CODE: CONTROL-FLOW GRAPHS

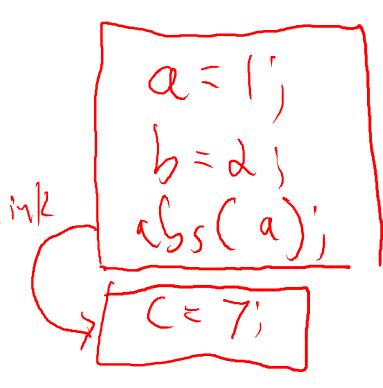
## TWO DISTINGUISHED INSTRUCTIONS IN A BLOCK (MAY BE THE SAME INSTRUCTION)

• Leader: An instruction that begins the block

The first instruction in the procedure The target of a jump The instruction after an terminator

• Terminator: An instruction that ends the block

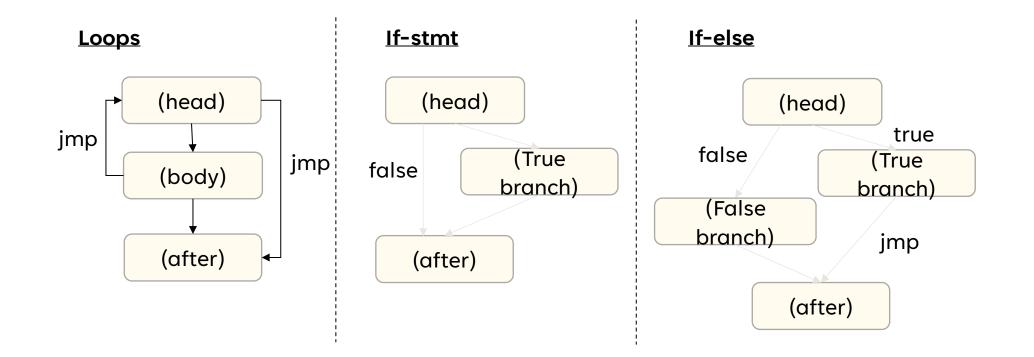
The last instruction of the procedure A jump (goto, if statement, loop construct) A call (We'll use a special LINK edge)



### **BENEFITS OF BASIC BLOCKS**

ABSTRACTING CODE: CONTROL-FLOW GRAPHS

HIGH-LEVEL VISUALIZATION OF CONTROL CONSTRUCTS



### **NOTE CFGS ARE PER-FUNCTION OBJECTS**

**ABSTRACTING CODE: CONTROL-FLOW GRAPHS** 

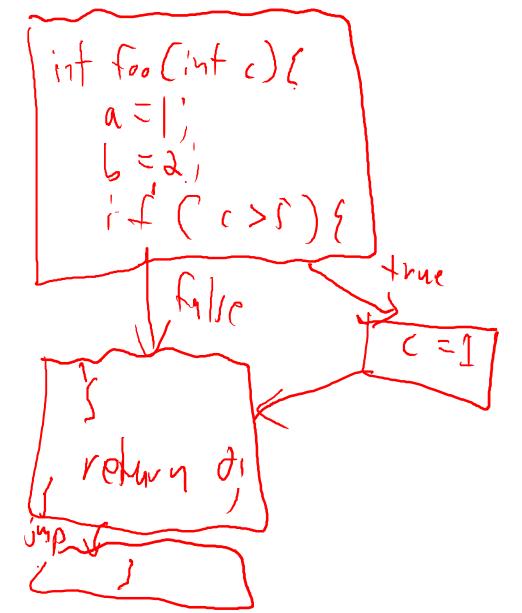
```
int foo(int c) {
  a = 1;
 b = 2;
  if (c > 5) {
    c = 1;
  return 0;
}
int main() {
  int local = 1;
  int ret = foo(local);
  if (ret > 1) {
    return 1;
  return 2;
}
```

1 CFG for foo
 1 CFG for main
 Special "link edge" to connect call to its return site

### **EXERCISE: BUILD THE CFG**

ABSTRACTING CODE: CONTROL-FLOW GRAPHS

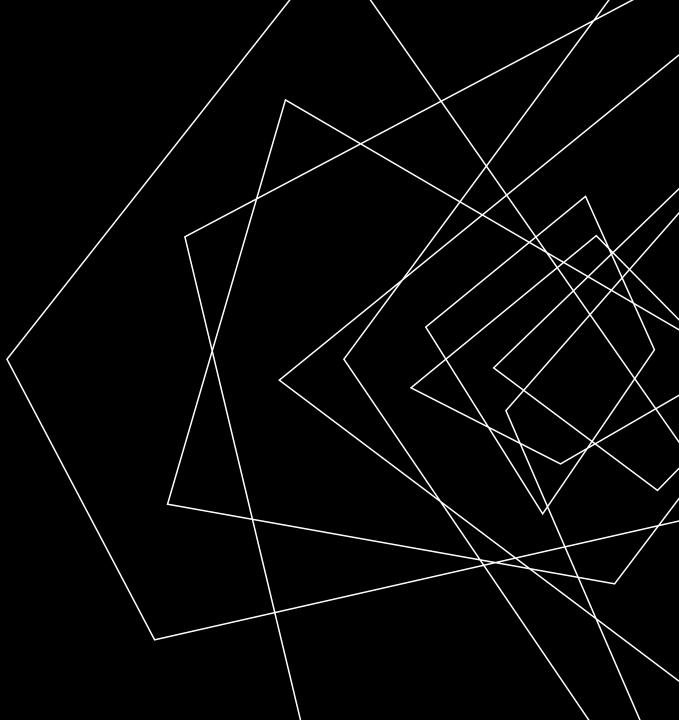
```
int foo(int c){
  a = 1;
 b = 2;
  if (c > 5) {
    c = 1;
  return 0;
int main() {
  int local =
 int ret = foo(local);
  if (ret > 1)
    return 1;
  return 2;
```

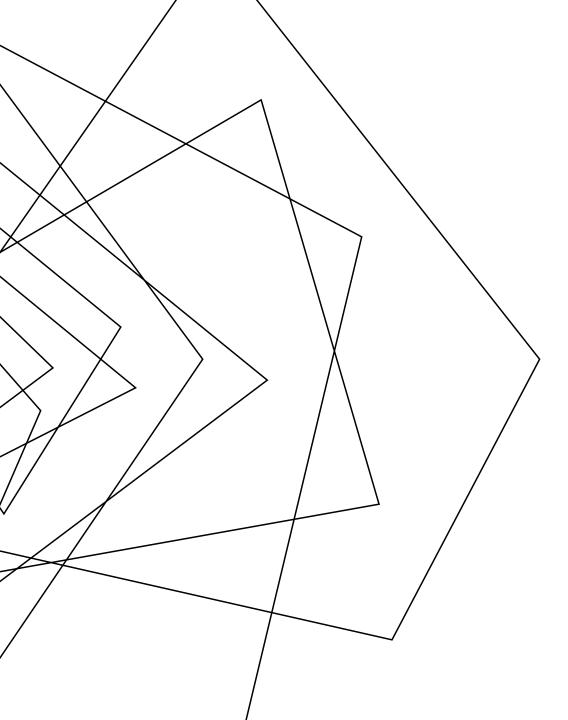


f00

## **LECTURE END!**

- Instruction Flowcharts
- Control Flow Graphs

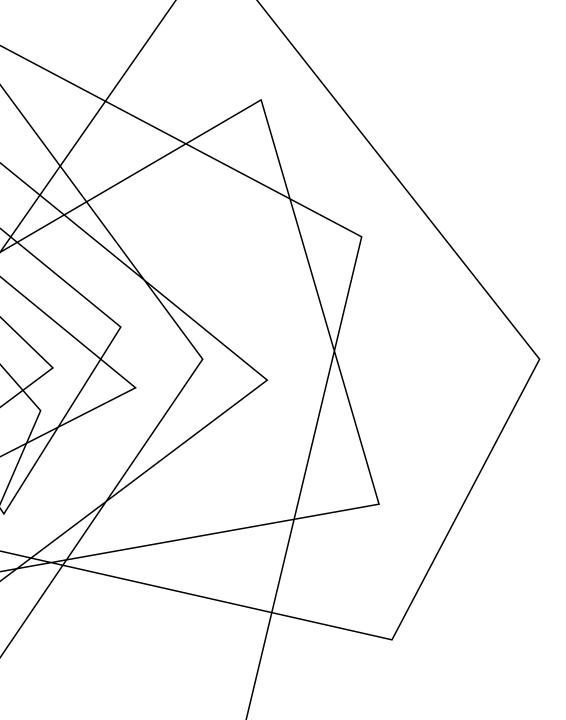




#### SUMMARY

DESCRIBED THE NEED TO VISUALIZE PROGRAMS IN WAYS OTHER THAN A FLAT LISTING OF SOURCE CODE

SHOWED ONE SUCH VISUALIZATION, THE CONTROL-FLOW GRAPH



### NEXT TIME

SHOW ADDITIONAL PROGRAM ABSTRACTIONS TO SIMPLIFY ANALYSIS, IN PARTICULAR SSA FORM

USE THESE CONCEPTS TO INTRODUCE LLVM IR