EXERCISE #5

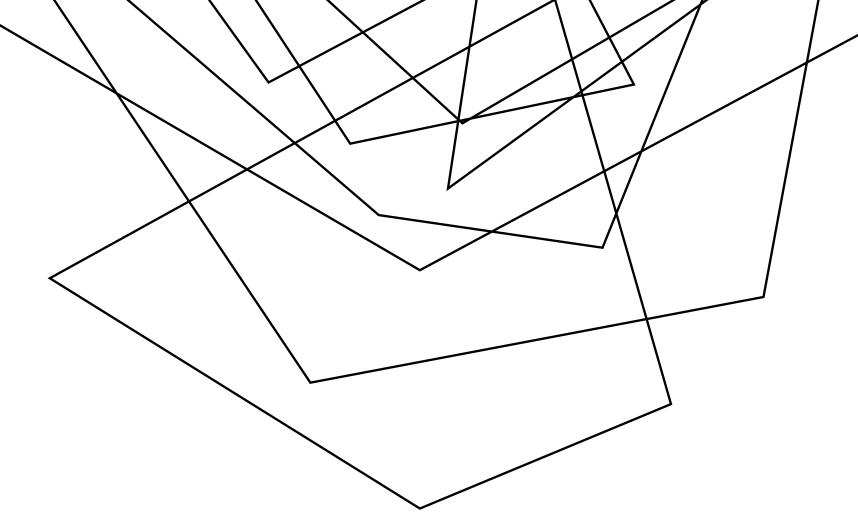
STATIC ANALYSIS REVIEW

Write your name and answer the following on a piece of paper

• Show the instruction flowchart of the following function

```
void v(int a) {
    if (a < 2) {
        while (c < 3) {
            c++;
        }
        if (b > 3) {
            c = 12;
        }
        return;
}
```

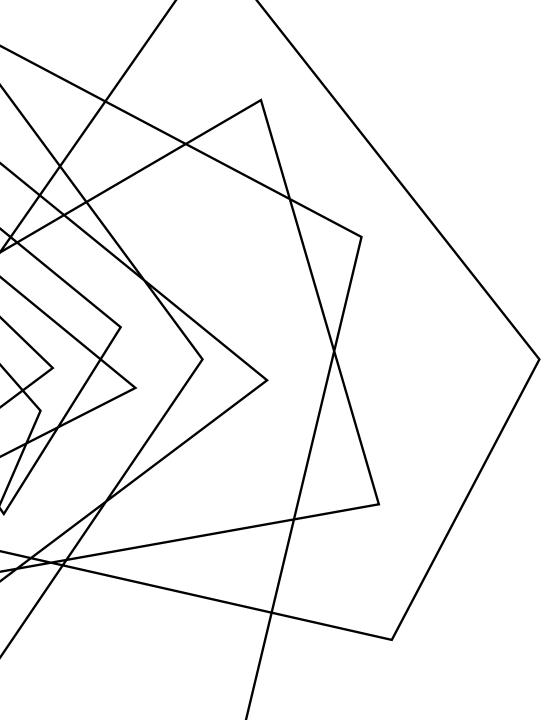
ADMINISTRIVIA AND ANNOUNCEMENTS



CONTROL FLOW GRAPHS

EECS 677: Software Security Evaluation

Drew Davidson



CLASS PROGRESS

OVERVIEWED TWO ANALYSIS APPROACHES:

- DYNAMIC ANALYSIS: ANALYSIS THAT USES A RUN OF THE PROGRAM
- STATIC ANALYSIS: ANALYSIS WITHOUT RUNNING THE PROGRAM

CONTINUE TO EXPLORE STATIC ANALYSIS

LOOK INTO CONCRETE FORMS OF STATIC ANALYSIS

- Particularly interested in dataflow analysis for now
- Building up the underlying abstractions / techniques to perform such analysis



OPPORTUNITIES OF STATIC ANALYSIS

CLASS PROGRESS

FINITE ABSTRACTIONS OF UNBOUNDED STATE SPACE

- Unnecessary to supply a given program input
- Summarize the behavior of the program under ANY input



LAST TIME: STATIC ANALYSIS

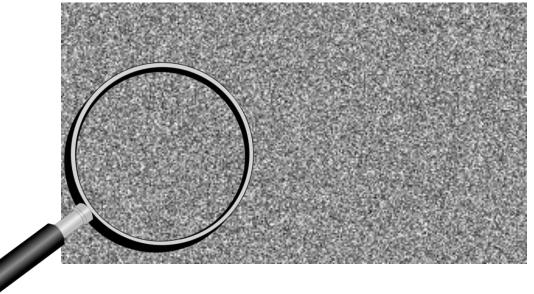
REVIEW: STATIC ANALYSIS

Mentioned some static analysis Techniques

- Syntactic Analysis
- Dataflow Analysis
- Model Checking

STARTED BUILDING A FUNDAMENTAL UNIT OF STATIC ANALYSIS: THE BASIC BLOCK

- Sequence of code that executes... sequentially



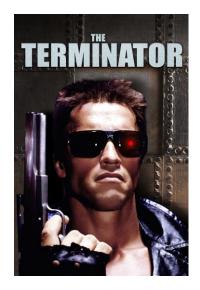
BASIC BLOCKS BOUNDARIES

REVIEW: STATIC ANALYSIS

TWO DISTINGUISHED INSTRUCTIONS IN A BLOCK (MAY BE THE SAME INSTRUCTION)

- Leader: An instruction that begins the block
- Terminator: An instruction that ends the block





BASIC BLOCKS BOUNDARIES

REVIEW: STATIC ANALYSIS

TWO DISTINGUISHED INSTRUCTIONS IN A BLOCK (MAY BE THE SAME INSTRUCTION)

- Leader: An instruction that begins the block
 - The first instruction in the procedure The target of a jump
 - The instruction after an terminator
- Terminator: An instruction that ends the block
 - The last instruction of the procedure
 - A jump (ifz, goto)
 - A call (We'll use a special LINK edge)

BASIC BLOCKS EXAMPLE

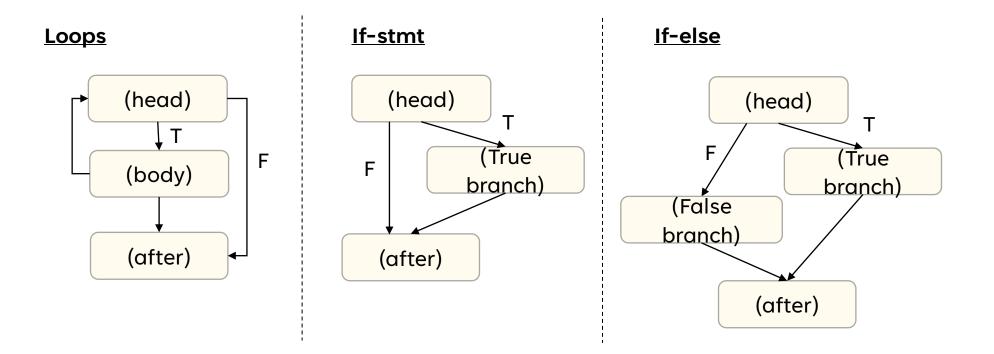
STATIC ANALYSIS: CONTROL FLOW GRAPHS

BENEFITS OF BASIC BLOCKS

STATIC ANALYSIS: CONTROL FLOW GRAPHS

AN ADDITIONAL ABSTRACTION LAYER

• Leader: An instruction that begins the block



CFGS: A PER-FUNCTION ABSTRACTION

STATIC ANALYSIS: CONTROL FLOW GRAPHS

BY DEFINITION, A CFG NEVER INCLUDES MULTIPLE FUNCTIONS

Call instruction simply has a special "link" edge to its successor

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CFG-Like analysis is possible on multiple functions, but requires special care to avoid infeasible paths

Source
$$\xi$$

Share ξ
in the second second

becture outline

- (Local) Dataflow analysis
- Global dataflow analysis

DATAFLOW ANALYSIS: BIG IDEA

DATAFLOW ANALYSIS

VIEW EACH STATEMENT AS A DATA TRANSFER FUNCTION

- Transform a program state into a new (updated) program state
- Simple idea: concrete program state into a new concrete program state

state M	
y has the value 1	
Stmt ₁ : x = y ;	
state M'	

x has the value y has the value	1
y has the value	1

COMPOSING TRANSFER FUNCTIONS

DATAFLOW ANALYSIS

Statements Compose Naturally with each other $\ensuremath{^{\star}}$

<u>state M</u>

y has the value 1

Stmt₁: x = y ; Stmt₂: z = x ;



x has the value 1 y has the value 1 z has the value 1



For now, we'll only think about analysis within a BBL

x share

AN EARLY WIN DATAFLOW ANALYSIS

EVEN WITH THIS VERY SIMPLE CONCEPT, MIGHT BE ABLE TO DETECT SOME ISSUES

state M y has the value 1 Stmt₁: x = y; $\gamma = 1$ x = 1Stmt₂: z = 0; y = 1 z = 0Stmt₃: p = 1 / z;

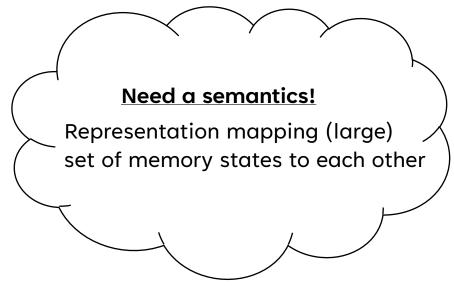
FORMALIZING TRANSFER FUNCTIONS

DATAFLOW ANALYSIS

IF WE WANT TO BUILD AN AUTOMATED (LOCAL) DATAFLOW ANALYSIS, WE NEED PROGRAMMATIC PRECISION

- Some sort of specification of what a statement does
- A statement is a memory state transformer

Memory state M Stmt₁: k += 1 ; Memory state M'



Depend somewhat on the analysis Goals:

- Keep states manageable
- Handle the uncertainty inherent in static analysis

MEMORY AS VALUE SETS

DATAFLOW ANALYSIS

LET EACH MEMORY LOCATION CORRESPOND TO A SET OF VALUES IT MIGHT CONTAIN

- Define (informally) transfer functions as mapping elements of M to elements of M'

We're still kinda-dodging the larger semantic questions here, for now lets just say we're using a big ol' if statement to define an operator

Memory state M
$$\langle k: \{1\} \rangle$$
 $\langle k: \{3,4\} \rangle$ Stmt1: k += 1 ;Memory state M' $\langle k: \{2\} \rangle$ $\langle k: \{4,5\} \rangle$

COMPOSING VALUE SETS

DATAFLOW ANALYSIS

 $\langle \chi: \xi: \xi \rangle$

 $Stmt_1: x = y;$

$$< \chi : \{1, \xi\}, \chi : \{1, \xi\} >$$

Stmt₂: z = λ ;
< $\chi : \{1, \xi\}, \chi : \{1, \xi\}, z : \{0\} >$

 $Stmt_{3}: p = 1 / z;$

MODELLING UNCERTAINTY

DATAFLOW ANALYSIS

WE CAN NOW HANDLE OPAQUE DATA SOMEWHAT CLEANLY

 $z = \frac{O_{1}}{2}$ Stmt₁: x = y; $z : \langle O \rangle >$ Stmt₂: z = USER_INPUT; $\zeta z : \langle O \rangle = \frac{1}{2}$ Stmt₂: z = global; $\zeta z : \langle D \downarrow D \downarrow V \downarrow D \downarrow T \land A \land D \downarrow D \downarrow \rangle$ Stmt₃: p = 1/z;
Stmt₃: p = 1/z;

LECTURE OUTLINE

- (Local) Dataflow analysis
- Global dataflow analysis

COMPOSING BLOCKS

VALUE-SET MODEL OF MEMORY IMPLIES AN EASY WAY TO EXTEND BEYOND LOCAL ANALYSIS

01. int x = 2; 02. if (g) { 03. x = x - 1; 04. if (g2) { 05. x = x - 1; 06. } 07. } 08. return 1 / x;

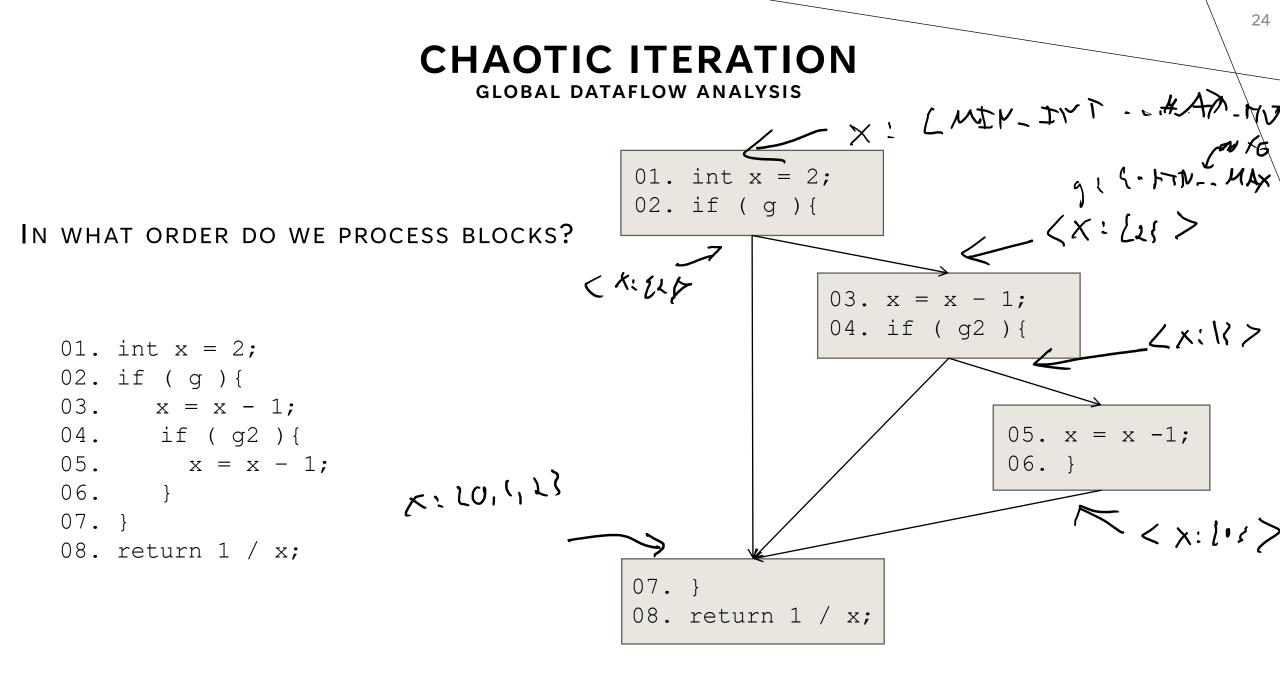


Go Global

COMPOSING BLOCKS

VALUE-SET MODEL OF MEMORY IMPLIES AN EASY WAY TO EXTEND BEYOND LOCAL ANALYSIS

```
01. int x = 2;
02. if ( g ) {
03.  x = x - 1;
04.  if ( g2 ) {
05.      x = x - 1;
06.    }
07. }
08. return 1 / x;
```



TROUBLE ON THE HORIZON GLOBAL DATAFLOW ANALYSIS



LOOPS ARE TOUGH TO HANDLE! GLOBAL DATAFLOW ANALYSIS

ISSUES WITH LOOPS

- Generate lots of paths
- Cyclic data dependency



Oh, brother! You may have some loops

LECTURE END!

- Local Dataflow analysis
- Global Dataflow analysis

